Intel® Firmware Support Package (Intel® FSP) for Intel® Atom™ Processor E3800 Product Family GOLD3

Release Notes
24 November 2014

Version History
These are the main releases of the Intel® Firmware Support Package (Intel® FSP) for Intel® Atom™ Processor E3800 Product Family:

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<tr>
<th>Date</th>
<th>Revision</th>
<th>Description</th>
</tr>
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<tbody>
<tr>
<td>29 September 2014</td>
<td>GOLD3</td>
<td>Gold 003 Release</td>
</tr>
<tr>
<td>10 January 2014</td>
<td>GOLD2</td>
<td>Gold 002 Release (First public release)</td>
</tr>
<tr>
<td>11 October 2013</td>
<td>GOLD1</td>
<td>Gold 001 Release</td>
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Intended Audience
Platform and system developers who intend to use an Intel® FSP-based boot loader for the firmware solution for their overall design based on the Intel® Atom™ Processor E3800 Product Family. This group includes, but is not limited to, system BIOS developers, boot loader developers, and system integrators.

Customer Support
For technical support, see the Intel® Embedded Design Center (Intel® EDC) Support website at:

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1 Introduction

This package contains required binary image(s) and collateral for the Intel® Firmware Support Package (Intel® FSP) for Intel® Atom™ Processor E3800 Product Family.

This document provides system requirements, installation instructions, issues and limitations, and legal information.

To learn more about this product, see:

- New features listed in the New in this Release section below.
- Reference documentation listed in the Related Documentation, Tools, and Packages section below.
- Installation instructions listed in the How to Install this Release section below.
2 New in This Release

New Features
This release includes the following new features and product changes:

- Support for the C0 and D0 stepping Intel® Atom™ Processor E3800 System on a Chip (SoC)
- Support for Android
- Additional FSP configuration parameters
- Memory Down configuration parameters moved from Vital Product Data (VPD) to Updatable Product Data (UPD)
3 Known Issues

None
4 Related Documentation, Tools, and Packages

- Intel® Firmware Support Package for Intel® Atom™ Processor E3800 Product Family Integration Guide
- Binary Configuration Tool (BCT) for Intel® FSP – Available at www.intel.com/fsp
Where to Find the Release
This package can be found at www.intel.com/fsp.

How to Install this Release
This release can be installed on either a Windows or a Linux system.

For Windows:
2. Run the .exe file to perform the installation.

For Linux:
2. Extract the contents of the .tgz file.
3. See the Readme_Extract.txt file for further instructions to complete the installation.
6 Release Content

This release package contains:

- FSP Binary
- Boot Setting File (BSF)
- FSP Integration Guide
- Release Notes
- Sample Code
- Intel® Atom™ Processor E3800 SoC Microcode
- Intel® Atom™ Processor E3800 Customer Reference Board (CRB) Platform Guide
- Intel® Atom™ Processor E3800 CRB Flash Image File
- Trusted Execution Engine (TXE) Binary
7 Hardware and Software Compatibility

Supported Hardware
The FSP included in this release is specifically targeted for the Intel® Atom™ Processor E3800 Product Family System on a Chip (SoC).

Supported Operating Systems
This release installs on either a Windows or a Linux system. However, the FSP binary itself can be used with any software development environment to generate a complete boot loader solution.
8 Configuration
A Binary Configuration Tool (BCT) for the Intel® FSP is provided as a companion tool and is intended to be used to:

- Customize the FSP binary configuration options based on the Boot Setting File (BSF).
- Rebase the FSP binary to a different base address (the default base address of the Intel® FSP for Intel® Atom™ Processor E3800 Product Family is 0xFFFFC0000).

It's recommended to use latest BCT with this release.

Please refer to the BCT User Guide for the usage instructions. See the Related Documentation, Tools, and Packages to obtain the BCT.
# Acronyms and Terms

The following acronyms and terms are used in this document (arranged in alphabetic order):

<table>
<thead>
<tr>
<th>Acronym/Term</th>
<th>Description</th>
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<tbody>
<tr>
<td>BCT</td>
<td>Binary Configuration Tool</td>
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<tr>
<td>BSF</td>
<td>Binary Settings File</td>
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<td>CRB</td>
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<tr>
<td>EDC</td>
<td>Embedded Design Center</td>
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<tr>
<td>FSP</td>
<td>Firmware Support Package</td>
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<tr>
<td>SoC</td>
<td>System on a Chip</td>
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<tr>
<td>TXE</td>
<td>Trusted Execution Engine</td>
</tr>
<tr>
<td>UPD</td>
<td>Updatable Product Data</td>
</tr>
<tr>
<td>VPD</td>
<td>Vital Product Data</td>
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10 Legal Information
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