
Release label: RANGELEY_FSP_POSTGOLD_001_20131218

December 18, 2013

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1. OVERVIEW
This package contains required binary image(s) and collateral for the Intel® Firmware Support Package (Intel® FSP) for the Intel® Atom™ Processor C2000 Product Family.


2. RELEASE INFORMATION
This Gold release has been integrated into a coreboot Proof of Concept (POC) bootloader and verified on the Mohon Peak Customer Reference Board (CRB).

It has also been validated in the Simics virtual environment.

3.  INTEGRATION NOTES
Several header and source files are provided in the package to assist with integrating the FSP with the bootloader.

Please consult the Intel® FSP Integration Guide for further information.

4.  CONFIGURATION
A Binary Configuration Tool for Intel® FSP is provided as a companion tool and is intended to be used to:

- Change configuration options based on provided BSF and FSP.
- Rebase the FSP to a different Base Address.

Please refer to the Binary Configuration Tool User Guide for the usage instructions.

5.  RMT SUPPORT
To enable Rank Margining Tool (RMT) according to document 535399 RMT User Guid v0.9 (Please contact Intel Representative):
- Open BSF file with BCT
  - In North Complex:
    - "Enable Rank Margin Tool" -> Enabled
    - "RMT CPGC exp_loop_cnt" -> 13
    - "RMT CPGC num_bursts" -> 13
  - In Platform Settings:
    - "Enable Serial Debug Messages" -> Enabled
- Save and patch the FSP binary file

6.  LIMITATIONS
- The Serial Port is at port 0x2F8.

7.  CHANGE LOG
Release RANGELEY_FSP_POSTGOLD_001_20131218: (December 18, 2013)
  - Rangeley FSP Post Gold Release.