Release Notes
19 December 2014

Version History/Revision History
These are the main releases of the Intel® Firmware Support Package (Intel® FSP) for Intel® Atom™ Processor C2000 Product Family:

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<th>Date</th>
<th>Revision</th>
<th>Description</th>
</tr>
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<tr>
<td>April 2, 2014</td>
<td>POSTGOLD2</td>
<td>Post-Gold 002 Release</td>
</tr>
<tr>
<td>December 18, 2013</td>
<td>POSTGOLD1</td>
<td>Post-Gold 001 Release – first public release</td>
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</tbody>
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Intended Audience
Platform and system developers who intend to use an Intel® FSP-based boot loader for the firmware solution for their overall design based on the Intel® Atom™ Processor C2000 Product Family. This group includes, but is not limited to, system BIOS developers, boot loader developers, and system integrators.

Customer Support
For technical support, see the Intel® Embedded Design Center (Intel® EDC) Support website at:

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1 Introduction

This package contains required binary image(s) and collateral for the Intel® Firmware Support Package (Intel® FSP) for the Intel® Atom™ Processor C2000 Product Family.

This document provides system requirements, installation instructions, issues and limitations, and legal information.

To learn more about this product, see:

- New features listed in the New in this Release section below, or in the help.
- Reference documentation listed in the Related Documentation, Tools, and Packages section below.
- Installation instructions listed in the How to Install this Release section below.
2 New in This Release

New Features
This release includes the following new features and product changes:

- Support for non-ECC SO-DIMMs
- New configuration items
3 Known Issues
The console serial port is fixed at IO port address 0x2F8.
4 Related Documentation, Tools, and Packages

- Binary Configuration Tool (BCT) for Intel® FSP – Available at www.intel.com/fsp
5 Where to Find the Release
This package can be found at www.intel.com/fsp.

How to Install this Release
This release can be installed on either a Windows or a Linux system.

For Windows:
2. Run the .exe file to perform the installation.

For Linux:
2. Extract the contents of the .tgz file.
3. See the Readme_Extract.txt file for further instructions to complete the installation.

How to Enable the Rank Margining Tool (RMT)
1. Start the Binary Configuration Tool (BCT) and open the RangeleyFsp.bsf file that was included with the FSP kit.
2. In the North Complex section:
   a. Set Enable Rank Margin Tool to Enabled.
   b. Set RMT CPGC exp loop cnt to 13.
   c. Set RMT CPGC num bursts to 13.
3. In the Platform Settings section:
   a. Set Enable Serial Debug Messages to Enabled.
5. Apply the settings to the FSP binary file.
6 Release Content
This release package contains:

- FSP Binary
- Boot Setting File (BSF)
- FSP Integration Guide
- Release Notes
- Sample Code
- Intel® Atom™ Processor C2000 SoC Microcode
7 Hardware and Software Compatibility

Supported Hardware
The FSP included in this release is specifically targeted for the Intel® Atom™ Processor C2000 Product Family System on a Chip (SoC).

Supported Operating Systems
This release installs on either a Windows or a Linux system. However, the FSP binary itself can be used with any software development environment to generate a complete boot loader solution.
8 Configuration

A Binary Configuration Tool (BCT) for the Intel® FSP is provided as a companion tool and is intended to be used to:

- Customize the FSP binary configuration options based on the Boot Setting File (BSF).
- Rebase the FSP binary to a different base address (the default base address of the Intel® FSP for Intel® Atom™ Processor C2000 Product Family is 0xFFF80000).

It's recommended to use latest BCT with this release.

Please refer to the BCT User Guide for the usage instructions. See the Related Documentation, Tools, and Packages to obtain the BCT.
## 9 Acronyms and Terms

The following acronyms and terms are used in this document (arranged in alphabetic order):

<table>
<thead>
<tr>
<th>Acronym/Term</th>
<th>Description</th>
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<tr>
<td>BCT</td>
<td>Binary Configuration Tool</td>
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<tr>
<td>BSF</td>
<td>Binary Settings File</td>
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<td>CRB</td>
<td>Customer Reference Board</td>
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<td>EDC</td>
<td>Embedded Design Center</td>
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<td>FSP</td>
<td>Firmware Support Package</td>
</tr>
<tr>
<td>RMT</td>
<td>Rank Margining Tool</td>
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<tr>
<td>SoC</td>
<td>System on a Chip</td>
</tr>
<tr>
<td>UPD</td>
<td>Updatable Product Data</td>
</tr>
<tr>
<td>VPD</td>
<td>Vital Product Data</td>
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10 Legal Information

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